

IAP15 Rec'd PCT/PTO 11 APR 2006

PTO/SB/08B (08-03)

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Application Number	
Filing Date	CONCURRENTLY
First Named Inventor	Reinder J. Bri
Art Unit	
Examiner Name	
Attorney Docket Number	US 030425

Sheet 1

of 1

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
/B.K./	A	J. W. S. LIU ET AL: "PERTS: A PROTOTYPING ENVIRONMENT FOR REAL-TIME SYSTEMS", REAL-TIME SYSTEMS SYMPOSIUM, 1993, PROCEEDINGS, RALEIGH DURHAM, NC, 12/1-3/1993, LOS ALAMITOS, CA	
		IEEE COMPUT. SOC, PAGES 184-188, XP010134703.	
/B.K./	B	G. J. NARLIKAR ET AL: "SPACE-EFFICIENT IMPLEMENTATION OF NESTED PARALLELISM", ACM SIGPLAN NOTICES, ASSOCIATION FOR COMPUTING MACHINERY, NY, VOL. 32, NO. 7,	
		JULY 1, 1997, PAGES 25-36, XP000701966.	

Examiner Signature	/Blake Kumabe/	Date Considered	05/03/2010
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